

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently Amended) An inverter apparatus comprising:

having a three-phase inverter configured to include paired including at least first and second pairs of switching elements connected in a bridge configuration, wherein the first and second pairs of switching elements and to convert a power supply voltage, which is supplied from a dc power supply, by said switching elements into to an ac voltage; characterized in that:

an inverter control portion circuit; and

a simultaneous switching prevention circuit that delays has a simultaneous switching prevention function of delaying a turn-on operation of each of said of a first switching element of the first pair of switching elements, wherein

the first switching element corresponds to a first phase and an electrode opposite an electrode corresponding to a second switching element of the second pair of switching elements,

the second switching element corresponds to a second phase, and

the delay is generated within a predetermined time after the second switching element of the second pair of switching elements is turned off

which correspond to phases other than a phase corresponding to an optional one of said switching elements and also correspond to an electrode opposite to an electrode

~~corresponding to said optional one of said switching elements, by a predetermined time in a case where a turn-on command signal for turning on each of said switching elements, which correspond to the other phases, is generated within a predetermined time period since turn-off of said optional one of said switching elements.~~

2. (Currently Amended) An inverter apparatus comprising:

~~having a three-phase inverter configured to include paired~~ including at least first and second pairs of switching elements connected in a bridge configuration, wherein the first and second pairs of switching elements ~~and to convert a power supply voltage, which is supplied from a dc power supply, by said switching elements into to an ac voltage;~~ characterized in that:

an inverter control portion circuit; and

a simultaneous switching prevention circuit that delays ~~has a simultaneous-switching prevention function of delaying a turn-off operation of each of said~~ of a first switching element of the first pair of switching elements, wherein

the first switching element corresponds to a first phase and an electrode opposite an electrode corresponding to a second switching element of the second pair of switching elements,

the second switching element corresponds to a second phase, and

the delay is generated within a predetermined time after the second switching element of the second pair of switching elements is turned on

~~which correspond to phases other than a phase corresponding to an optional one of said switching elements and also correspond to an electrode opposite to an electrode~~

~~corresponding to said optional one of said switching elements, by a predetermined time in a case where a turn-off command signal for turning off each of said switching elements, which correspond to the other phases, is generated within a predetermined time period since turn-on of said optional one of said switching elements.~~

3. (Currently Amended) An inverter apparatus comprising:

~~having a three-phase inverter configured to include paired~~ including at least first and second pairs of switching elements connected in a bridge configuration, wherein the first and second pairs of switching elements ~~and to convert a power supply voltage, which is supplied from a dc power supply, by said switching elements into to an ac voltage,~~ characterized in that:

~~an inverter control portion~~ circuit; and

~~a simultaneous switching prevention circuit that delays~~ has a simultaneous-switching prevention function of delaying a turn-off and a turn-on operation of each of said of a first switching element of the first pair of switching elements, wherein

the first switching element corresponds to a first phase and an electrode opposite an electrode corresponding to a second switching element of the second pair of switching elements,

the second switching element corresponds to a second phase,

if the switching prevention circuit delays the turn-on, the delay is generated within a predetermined time after the second switching element of the second pair of switching elements is turned off, and

if the switching prevention circuit delays the turn-off, the delay is generated within a predetermined time after the second switching element of the second pair of switching elements is turned on

~~which correspond to phases other than a phase corresponding to an optional one of said switching elements and also correspond to an electrode opposite to an electrode corresponding to said optional one of said switching elements, by a predetermined time in a case where a turn-on command signal for turning on each of said switching elements, which correspond to the other phases, is generated within a predetermined time period since turn-off of said optional one of said switching elements, and also delaying a turn-off operation of each of said switching elements, which correspond to phases other than a phase corresponding to an optional one of said switching elements and also correspond to an electrode opposite to an electrode corresponding to said optional one of said switching elements, by a predetermined time in a case where a turn-off command signal for turning off each of said switching elements, which correspond to the other phases, is generated within a predetermined time period since turn-on of said optional one of said switching elements.~~

4. (Canceled)

5. (Currently Amended) The inverter apparatus according to one of claims 1 [[to]] and 3, wherein ~~characterized in that:~~ the simultaneous switching prevention function is achieved by a simultaneous switching prevention circuit including comprises:

~~a predetermined-post-turning-off-time generating circuit, which is adapted to generate that generates a predetermined time that elapses after the turn-off of the second switching element; optional one of said switching elements and/or a predetermined-post-turning-on-time generating circuit which is adapted to generate a predetermined time that elapses after turn-on of the optional one of said switching elements; and~~

a simultaneous switching prevention logic circuit.

6. (Currently Amended) The inverter apparatus according to any one of claims 1-3, wherein said the first and second pairs of switching elements are Si-GTO elements.

7. (Currently Amended) The inverter apparatus according to any one of claims 1-3, wherein said the first and second pairs of switching elements are SiC-GTO elements.

8. (Currently Amended) The inverter apparatus according to any one of claims 1-3, wherein said the first and second pairs of switching elements are wide-gap semiconductors including diamond and GaN.

9. (New) The inverter apparatus according to one of claims 2 and 3, wherein the simultaneous switching prevention circuit comprises:

a predetermined-post-turning-on-time generating circuit that generates a predetermined time that elapses after the turn-on of the second switching element; and

a simultaneous switching prevention logic circuit.